

PIC 12C508/12C509 – CALM instructions

13 décembre 1900 pic50xcalm

Code	14 bits Microchip	CALM	Flags
1100	kkkkkkkk	movlw val	Move #Val,W [none]
0000	001fffff	movwf reg	Move W,Reg [none]
0010	000fffff	movf reg,0	Move Reg,W [Z]
0010	001fffff	movf reg,1	Test Reg [Z] Move Reg,Reg
0000	00000010	option	[none]
0000	00000110	tris gpio	Move W,Tris [none]
0001	110fffff	addwf reg,0	Add Reg,W [C,D,Z] Reg+W -> W
0001	111fffff	addwf reg,1	Add W,Reg [C,D,Z] Reg+W -> Reg
0000	10fffff	subwf reg,0	Sub W,Reg,W [C',D,Z] Reg+(-W) -> W
0000	101fffff	subwf reg,1	Sub #Val,W,Reg [C',D,Z] Reg+(-W) -> Reg
1110	kkkkkkkk	andlw val	And #Val,W [Z]
0001	010fffff	andwf reg,0	And Reg,W [Z]
0001	011fffff	andwf reg,1	And W,Reg [Z]
1110	kkkkkkkk	iorlw val	Or #Val,W [Z]
0001	000fffff	iorwf reg,0	Or Reg,W [Z]
0001	001fffff	iorwf reg,1	Or W,Reg [Z]
1110	kkkkkkkk	xorlw val	Xor #Val,W [Z]
0001	100fffff	xorwf reg,0	Xor Reg,W [Z]
0001	101fffff	xorwf reg,1	Xor W,Reg [Z]
0011	100fffff	swapf reg,0	Swap Reg,W [none]
0011	101fffff	swapf reg,1	Swap Reg,Reg [none]
0000	0011ffff	clr reg	Clr Reg [Z=1]
0000	01000000	clrw	Clr W [Z=1]
0000	00000100	clrwtd	Clr WDT [T0=1, PD=1]
0000	00000011	sleep	Clr WDT,OSC [T0=1, PD=0]
0010	011fffff	comf reg,1	Not Reg [Z]
0010	010fffff	comf reg,0	Not Reg,W [Z]
0010	101fffff	incf reg,1	Inc Reg [Z] Reg+1 -> Reg
0010	100fffff	incf reg,0	Inc Reg,W [Z] Reg->Reg Reg+1->W
0000	111fffff	decf reg,1	Dec Reg [Z] Reg-1 -> Reg
0000	110fffff	decf reg,0	Dec Reg,W [Z] Reg->Reg Reg-1->W
0011	011fffff	rlf reg,1	RLC Reg [C]
0011	010fffff	rlf reg,0	RLC Reg,W [C]
0011	001fffff	rrf reg,1	RRC Reg [C]
0011	000fffff	rrf reg,0	RRC Reg,W [C]
0100	bbbfffff	bcf reg,b	Clr Reg:#b [none]
0101	bbbfffff	bsf reg,b	Set Reg:#b [none]
0011	111fffff	incfsz reg,1	IncSkip,EQ Reg [none]
0011	110fffff	incfsz reg,0	IncSkip,EQ Reg,W [none]
0010	111fffff	decfsz reg,1	DecSkip,EQ Reg [none]
0010	110fffff	decfsz reg,0	DecSkip,EQ Reg,W [none]
0110	bbbfffff	btfsz reg,b	TestSkip,BC Reg:#b [none]
0111	bbbfffff	btfss reg,d	TestSkip,BS Reg:#b [none]
101k	kkkkkkkk	goto addr	Jump Addr [none]
1000	kkkkkkkk	call addr	Call Addr ² [none]
1101	kkkkkkkk	retlw val	RetMove #Val,W [none]
000000	00000000	nop	Nop [none]
	bcf status,0	CLRC	Clr F:#0
	bsf status,0	SETC	Set F:#0
		CLRZ SETZ CLRZ SETD	Clr F:#2 etc.
	btfsz status,0	Skip,CS	TestSkip,BS F:#0
	btfss status,0	Skip,CC	TestSkip,BC F:#0
	btfsz status,1	Skip,DS	TestSkip,BS F:#1
	btfss status,1	Skip,DC	TestSkip,BC F:#1
	btfsz status,2	Skip,EQ	TestSkip,BS F:#2
	btfsz status,2	Skip,NE	TestSkip,BC F:#2

